EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|---------|-------------------|--------------------|---------------------|---------|------------------|
| L1 | 22560 | memory adj module | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:35 |
| L2 | 1690567 | group | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:35 |
| L3 | 1630 | 1 same 2 | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |
| L4 | 12201 | chip adj select | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |
| L5 | 224 | 3 and 4 | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |
| L6 | 17 | park-myun-joo.in. | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |
| L7 | 50 | so-byung-se.in. | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |
| L8 | 27 | lee-jae-jun.in. | US-PGPUB; USPAT | OR | ON | 2006/07/23 11:36 |

Day : Sunday Date: 7/23/2006

Time: 11:38:58



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = PARK First Name = MYUN

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------|---------------|--------|------------|---|--------------------|
| 09799094 | 6815621 | 150 | 03/06/2001 | CHIP SCALE PACKAGE, PRINTED CIRCUIT BOARD, AND METHOD OF DESIGNING A PIINTED CIRCUIT BOARD | PARK, MYUN JOO |
| 10471373 | Not Issued | 41 | 09/09/2003 | Door and a drum type washing machine and a clothes dryer using the same | PARK, MYUN SIK |
| 09777446 | 6772262 | 150 | 02/06/2001 | MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE | PARK, MYUN- JOO |
| 09777547 | 6414904 | 150 | 02/06/2001 | TWO CHANNEL MEMORY SYSTEM HAVING SHARED CONTROL AND ADDRESS BUS AND MEMORY MODULES USED THEREFOR | PARK, MYUN- JOO |
| 09851277 | Not Issued | 41 | 05/08/2001 | Memory interface systems that couple a memory to a memory controller and are responsive to a terminal voltage that is independent of supply voltages for the memory and the memory controller | PARK, MYUN- JOO |
| 09858401 | 6480409 | 150 | 05/16/2001 | MEMORY MODULES HAVING INTEGRAL TERMINATING RESISTORS AND COMPUTER SYSTEM BOARDS FOR USE WITH SAME | PARK, MYUN- JOO |
| 10200731 | 6870742 | 150 | 07/22/2002 | SYSTEM BOARD | PARK, MYUN- JOO |
| 10353924 | 6828819 | 150 | | HIGH-SPEED MEMORY SYSTEM | PARK, MYUN- JOO |
| 10424923 | Not Issued | 61 | 04/29/2003 | Semiconductor memory device with data bus scheme for reducing high frequency noise | PARK, MYUN- JOO |
| | | | | | |

| 10629866 | Not Issued | 71 | | Memory system having memory modules with different memory device loads | PARK, MYUN- JOO |
|----------|---------------|-----|------------|--|---------------------|
| 10644735 | Not Issued | 71 | | Semiconductor memory system having multiple system data buses | PARK, MYUN- JOO |
| 10883488 | 6990543 | 150 | 07/01/2004 | MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE | PARK, MYUN- JOO |
| 08482393 | 5577039 | 150 | 06/07/1995 | SYSTEM AND METHOD OF SIGNAL TRANSMISSION WITHIN A PLESIOCHRONOUS DIGITAL HIERARCHY UNIT USING ATM ADAPTATION LAYERS | PARK, MYUNG C. |
| 11429005 | Not Issued | 20 | 05/05/2006 | Method and apparatus for performing an instant messenger function using a local area network | PARK, MYUNG EUN |
| 09950478 | 6677181 | 150 | 09/10/2001 | STACK PACKAGE AND METHOD FOR FABRICATING THE SAME | PARK, MYUNG GEUN |
| 11291612 | Not Issued | 20 | 12/01/2005 | Stack type package module and method for manufacturing the same | PARK, MYUNG GEUN |
| 09309399 | 6316825 | 150 | 05/10/1999 | CHIP STACK PACKAGE UTILIZING A CONNECTING HOLE TO IMPROVE ELECTRICAL CONNECTION BETWEEN LEADFRAMES | PARK, MYUNG GEUN |
| 08843738 | Not Issued | 161 | 11 | HIGH EFFICIENCY PROCESS AND APPARATUS FOR TREATING NITROGEN AND PHOSPHOROUS CONTAINED IN SEWAGE | PARK, MYUNG GYUN |
| 09164752 | 6177007 | 150 | 10/01/1998 | HIGH EFFICIENCY APPARATUS FOR TREATING NITROGEN AND PHOSPHOROUS CONTAINED IN SEWAGE | PARK, MYUNG GYUN |
| 08300453 | Not Issued | 166 | 09/02/1994 | CRT ELECTRON GUN FOR CONTROLLING DIVERGENCE ANGLE OF ELECTRON BEAMS ACCORDING TO INTENSITY OF CURRENT | PARK, MYUNG H |
| 06637569 | Not Issued | 161 | 08/03/1984 | LONG-LIFE INCANDESCENT LAMP OF IMPROVED | PARK, MYUNG H. |

| | | | | OPERATIONAL SAFETY | |
|----------|---------------|-----|------------|---|----------------|
| 07598887 | Not Issued | 166 | 10/19/1990 | QUINOLONE COMPOUNDS AND A PROCESS FOR THEIR PREPARATION | PARK, MYUNG H. |
| 07598888 | 5089621 | 150 | 10/19/1990 | DIAZABICYCLO AMINE COMPOUNDS WHICH ARE INTERMEDIATES FOR ANTI- BACTERIAL COMPOUNDS | PARK, MYUNG H. |
| 07599225 | 5091384 | 150 | 10/19/1990 | ANTI-BACTERIAL QUINOLONE- AND NAPHTHYRIDONE- CARBOXYLIC ACID COMPOUNDS | PARK, MYUNG H. |
| 07905185 | Not Issued | 164 | 11 | ANTI-BACTERIAL PYRIDO [1,2,3-DE]-1,4-BENZOXAZINES | PARK, MYUNG H. |
| 07997961 | Not Issued | 161 | 12/29/1992 | NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME | PARK, MYUNG H. |
| 07997962 | Not Issued | 161 | 12/29/1992 | NOVEL AZASPIRO DERIVATIVES AND PROCESSES FOR PREPARING SAME | PARK, MYUNG H. |
| 07998231 | 5344846 | 150 | 12/30/1992 | COMPOSITIONS AND METHODS FOR INHIBITING DEOXYHYPUSINE SYNTHASE AND THE GROWTH OF CELLS | PARK, MYUNG H. |
| 08073207 | 5409933 | 250 | 06/08/1993 | NOVEL QUINOLINE DERIVATIVES AND PROCESSES FOR PREPARING THE SAME | PARK, MYUNG H. |
| 08187125 | Not Issued | 161 | 01/27/1994 | NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME | PARK, MYUNG H. |
| 08188128 | Not Issued | 161 | 01/28/1994 | NOVEL AZASPIRO DERIVATIVES AND PROCESSES FOR PREPARING SAME | PARK, MYUNG H. |
| 08349445 | Not Issued | 166 | 12/05/1994 | ELECTRON GUNS FOR COLOR PICTURE TUBE | PARK, MYUNG H. |
| 08756589 | 5710481 | 150 | 11/27/1996 | CRT ELECTRON GUN FOR CONTROLLING DIVERGENCE ANGLE OF ELECTRON BEAMS | PARK, MYUNG H. |

| 1 | | | | ACCORDING TO INTENSITY OF CURRENT | |
|----------|---------------|-----|------------|---|---------------------|
| 60314561 | Not Issued | 159 | 08/23/2001 | Composition and method for inhibiting angiogenesis and cell proliferation | PARK, MYUNG HEE |
| 60748879 | Not Issued | 20 | 12/09/2005 | Deoxyhypusine hydroxylase | PARK, MYUNG HEE |
| 09714429 | 6624799 | 150 | 11/17/2000 | RADIO FREQUENCY PLASMA DISPLAY PANEL | PARK, MYUNG HO |
| 11354036 | Not Issued | 20 | EI I | Electroless metal film-plating system | PARK, MYUNG HO |
| 08711063 | Not Issued | 166 | | ELECTRON GUNS FOR COLOR PICTURE TUBE | PARK, MYUNG HO |
| 08890283 | 5736812 | 150 | 07/09/1997 | ELECTRON GUNS FOR COLOR PICTURE TUBE | PARK, MYUNG HO |
| 08950975 | 6013309 | 150 | 10/15/1997 | PROTECTION LAYER OF PLASMA DISPLAY PANEL AND METHOD OF FORMING THE SAME | PARK, MYUNG HO |
| 09340045 | 6473061 | 150 | 06/28/1999 | PLASMA DISPLAY PANEL DRIVE METHOD AND APPARATUS | PARK, MYUNG HO |
| 09442747 | 6379783 | 150 | 11/18/1999 | PROTECTION LAYER OF PLASMA DISPLAY PANEL AND METHOD OF FORMING THE SAME | PARK, MYUNG HO |
| 10482782 | Not Issued | 41 | 08/24/2004 | Method for processing ginseng and the uses of extract of processed ginseng | PARK, MYUNG HWAN |
| 07997963 | Not Issued | 161 | 12/29/1992 | NOVEL QUINOLONE CARBOXYLIC ACID DERIVATIVES AND PROCESSES FOR PREPARING SAME | PARK, MYUNG HWAN |
| 11119149 | Not Issued | 30 | 04/29/2005 | Semiconductor nano-structure and method of forming the same | PARK, MYUNG IL |
| 07302468 | 5026955 | 150 | 01/27/1989 | DRIVE CONTROL METHOD FOR AN ELECTROMAGNETIC INDUCTION HEATING APPARATUS | PARK, MYUNG JONG |
| 06829908 | Not Issued | 161 | 02/18/1986 | DRIVING CIRCUIT OF THE INVERTER FOR PULSATING CURRENT SOURCE-TYPE DISCHARGE LAMP | PARK, MYUNG K. |
| 06857378 | Not | 161 | 04/30/1986 | HIGH POWER FACTOR | PARK, MYUNG K. |

| | Issued | <u> </u> | | RECTIFYING CIRCUIT | |
|----------|---------------|----------|------------|---|---------------------|
| 07030678 | Not Issued | 161 | | HIGH POWER FACTOR BALLAST CIRCUIT OPERATING UNDER PULSATING DC POTENTIAL | PARK, MYUNG K. |
| 10499703 | Not Issued | 30 | 06/21/2004 | Fuel cell | PARK, MYUNG SEOK |

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Inventor Name Search Result

Your Search was:

Last Name = SO

First Name = BYUNG

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|-----------------|---------|--------|------------|---|---------------|
| 09799094 | 6815621 | 150 | 03/06/2001 | CHIP SCALE PACKAGE, PRINTED CIRCUIT BOARD, AND METHOD OF DESIGNING A PIINTED CIRCUIT BOARD | SO, BYUNG SE |
| 08773787 | 5856982 | 150 | 12/24/1996 | HIGH-SPEED DISTURB TESTING METHOD AND WORD LINE DECODER IN SEMICONDUCTOR MEMORY DEVICE | SO, BYUNG-SE |
| 09426609 | 6714595 | 150 | 10/26/1999 | SIGNAL TRANSMISSION CIRCUITS THAT USE MULTIPLE INPUT SIGNALS TO GENERATE A RESPECTIVE TRANSMIT SIGNAL AND METHODS OF OPERATING THE SAME | SO, BYUNG-SE |
| 09454339 | 6587976 | 150 | 12/03/1999 | SEMICONDUCTOR DEVICE TESTER FOR MEASURING SKEW BETWEEN OUTPUT PINS OF A SEMICONDUCTOR DEVICE | SO, BYUNG-SE |
| <u>09540988</u> | 6252805 | 150 | 03/31/2000 | Semiconductor memory device including programmable output pin determining unit and method of reading the same during test mode | SO, BYUNG-SE |
| 09612610 | 6382986 | 150 | 07/08/2000 | Socket for mounting memory module boards on a printed circuit board | SO, BYUNG-SE |
| 09688297 | 6632705 | 150 | 10/13/2000 | MEMORY MODULES AND PACKAGES USING DIFFERENT ORIENTATIONS AND TERMINAL ASSIGNMENTS | SO, BYUNG-SE |

| 09777446 | 6772262 | 150 | it i | MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE | SO, BYUNG-SE |
|----------|---------------|-----|------------|---|--------------|
| 09777547 | 6414904 | 150 | 02/06/2001 | TWO CHANNEL MEMORY SYSTEM HAVING SHARED CONTROL AND ADDRESS BUS AND MEMORY MODULES USED THEREFOR | SO, BYUNG-SE |
| 09851277 | Not Issued | 41 | 05/08/2001 | Memory interface systems that couple a memory to a memory controller and are responsive to a terminal voltage that is independent of supply voltages for the memory and the memory controller | SO, BYUNG-SE |
| 09858401 | 6480409 | 150 | 05/16/2001 | MEMORY MODULES HAVING INTEGRAL TERMINATING RESISTORS AND COMPUTER SYSTEM BOARDS FOR USE WITH SAME | SO, BYUNG-SE |
| 10043047 | Not Issued | 41 | 01/09/2002 | Memory system having stub bus configuration | SO, BYUNG-SE |
| 10074309 | 6754112 | 150 | 02/11/2002 | INTEGRATED CIRCUIT DEVICES HAVING DELAY CIRCUITS FOR CONTROLLING SETUP/DELAY TIMES OF DATA SIGNALS THAT ARE PROVIDED TO MEMORY DEVICES AND METHODS OF OPERATING SAME | SO, BYUNG-SE |
| 10094448 | 6944737 | 150 | 03/08/2002 | MEMORY MODULES AND METHODS HAVING A BUFFER CLOCK THAT OPERATES AT DIFFERENT CLOCK FREQUENCIES ACCORDING TO THE OPERATING MODE | SO, BYUNG-SE |
| 10200731 | 6870742 | 150 | 07/22/2002 | SYSTEM BOARD | SO, BYUNG-SE |
| 10353924 | 6828819 | 150 | 01/30/2003 | HIGH-SPEED MEMORY SYSTEM | SO, BYUNG-SE |
| 10629866 | Not Issued | 71 | 07/30/2003 | Memory system having memory modules with different memory device loads | SO, BYUNG-SE |
| 10644735 | Not Issued | 71 | 08/21/2003 | Semiconductor memory system having multiple system data buses | SO, BYUNG-SE |
| | | | | | |

| 10722159 | Not Issued | 80 | | Multi-chip package for reducing parasitic load of pin | SO, BYUNG-SE |
|----------|---------------|-----|------------|--|--------------|
| 10794680 | 7049849 | 150 | 03/05/2004 | SIGNAL TRANSMISSION CIRCUITS THAT USE MULTIPLE INPUT SIGNALS TO GENERATE A RESPECTIVE TRANSMIT SIGNAL | SO, BYUNG-SE |
| 10795507 | 6836138 | 150 | 03/09/2004 | MODULE HAVING TEST ARCHITECTURE FOR FACILITATING THE TESTING OF BALL GRID ARRAY PACKAGES, AND TEST METHOD USING THE SAME | SO, BYUNG-SE |
| 10831702 | Not Issued | 41 | 14 | Memory module and method of testing the same | SO, BYUNG-SE |
| 10833322 | Not Issued | 30 | | Buffered memory module and method for testing same | SO, BYUNG-SE |
| 10837610 | Not Issued | 83 | | Memory system with improved signal integrity | SO, BYUNG-SE |
| 10853353 | 7072201 | 150 | 05/26/2004 | MEMORY MODULE | SO, BYUNG-SE |
| 10883488 | 6990543 | 150 | 07/01/2004 | MEMORY MODULE WITH IMPROVED DATA BUS PERFORMANCE | SO, BYUNG-SE |
| 10900140 | Not Issued | 30 | 07/28/2004 | Memory module test system | SO, BYUNG-SE |
| 10913359 | Not Issued | 30 | 08/09/2004 | Data transmission system and method | SO, BYUNG-SE |
| 10975810 | Not Issued | 20 | 10/27/2004 | Memory module with registers | SO, BYUNG-SE |
| 10988390 | Not Issued | 41 | 11/12/2004 | Mounting structure in integrated circuit module | SO, BYUNG-SE |
| 10997406 | Not Issued | 41 | 11/24/2004 | Memory module system with efficient control of on-die termination | SO, BYUNG-SE |
| 11029008 | Not Issued | 71 | | Memory module, memory unit, and hub with non-periodic clock and methods of using the same | SO, BYUNG-SE |
| 11064671 | Not Issued | 93 | 02/24/2005 | MEMORY MODULE AND A METHOD OF ARRANGING A SIGNAL LINE OF THE SAME | SO, BYUNG-SE |
| 11102181 | Not Issued | 30 | 04/08/2005 | Memory module with memory devices of different capacity | SO, BYUNG-SE |
| 11118377 | Not Issued | 20 | | Method of testing a memory module and hub of the memory | SO, BYUNG-SE |

| | | | | module | |
|----------|---------------|-----|------------|--|---------------------|
| 11177736 | Not Issued | 30 | 07/08/2005 | Stacked board-on-chip package having mirroring structure and dual inline memory module on which the stacked board-on-chip packages are mounted | SO, BYUNG-SE |
| 11181059 | Not Issued | 30 | | Error detecting memory module and method | SO, BYUNG-SE |
| 11227225 | Not Issued | 30 | | Method of testing memory module and memory module | SO, BYUNG-SE |
| 11256580 | Not Issued | 25 | 10/21/2005 | Determining operation mode for semiconductor memory device | SO, BYUNG-SE |
| 11266428 | Not Issued | 30 | 11/04/2005 | Memory module with stacked semiconductor devices | SO, BYUNG-SE |
| 60267908 | Not Issued | 159 | 02/09/2001 | Memory system having stub bus configuration | SO, BYUNG-SE |
| 60579657 | Not Issued | 159 | 06/16/2004 | Method of testing memory module in transparent mode and hub of memory module of testing the same | SO, BYUNG-SE |
| 11229609 | Not Issued | 20 | 09/20/2005 | Apparatus and method of providing fingertip haptics of visual information using electroactive polymer for image display device | SOH, BYUNG SEOK |
| 11291927 | Not Issued | 20 | 12/02/2005 | Method and apparatus for controlling input/output interface | SOH, BYUNG SEOK |
| 11206076 | Not Issued | 30 | 08/18/2005 | Line-of-sight-based authentication apparatus and method | SOH, BYUNG SEOK |
| 10455434 | Not Issued | 71 | | Method of and apparatus for inputting character using pointing device | SOH, BYUNG- SEOK |
| 10540925 | Not Issued | 25 | 05/12/2006 | Method for configuring 3d input device, method for reconfiguring 3d input device, method for recognizing wearing of the 3d input device, and the apparatus thereof | SOH, BYUNG- SEOK |
| 10735906 | Not Issued | 41 | 12/16/2003 | Apparatus and method for detecting finger-motion | SOH, BYUNG- SEOK |
| 11043423 | Not Issued | 30 | 01/27/2005 | Method of adjusting pointing position during click operation and 3D input device using the same | SOH, BYUNG- SEOK |
| 11183171 | Not Issued | 20 | 07/18/2005 | Apparatus and method for providing haptics of image | SOH, BYUNG- SEOK |

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Inventor Name Search Result

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Last Name = LEE First Name = JAE

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------|---------------|--------|------------|--|---------------|
| 08955378 | 5939954 | 150 | | EQUIVALENT CIRCUIT OF PACKAGE GROUND TERMINAL PADDLE | LEE, JAE |
| 09080326 | 6046659 | 150 | 05/15/1998 | DESIGN AND FABRICATION OF BROADBAND SURFACE- MICROMACHINED MICRO- ELECTRO-MECHANICAL SWITCHES FOR MICROWAVE AND MILLIMETER-WAVE APPLICATIONS | LEE, JAE |
| 09531874 | Not Issued | 161 | 03/21/2000 | Magnetic point massager | LEE, JAE A. |
| 08227234 | Not Issued | 161 | 1 | MINI ACCU-PRESSURE MASSAGER | LEE, JAE A. |
| 29021250 | D369414 | 150 | 04/13/1994 | ACCU-PRESSURE MASSAGER | LEE, JAE A. |
| 29021256 | D361132 | 150 | 04/13/1994 | ACCU-PRESSURE MASSAGER | LEE, JAE A. |
| 29116123 | D430938 | 150 | 12/28/1999 | MAGNETIC POINT MASSAGER | LEE, JAE A. |
| 06935326 | Not Issued | 161 | 03/03/1987 | "SET-UP" CAN | LEE, JAE B. |
| 06941189 | Not Issued | 161 | 12/12/1986 | METHODIZED BOLT, NUT, SCREW AND SCREW-DRIVER | LEE, JAE B. |
| 07012152 | Not Issued | 161 | 04/28/1987 | EMERGENT HONK FOR TURN | LEE, JAE B. |
| 07017148 | Not Issued | 161 | 04/28/1987 | WATCHER TELEVISION | LEE, JAE B. |
| 07097837 | Not Issued | 161 | 09/17/1987 | METHODIZED BOLT, NUT, AND SCREW | LEE, JAE B. |
| 07097838 | Not Issued | 161 | 09/17/1987 | METHODIZED SCREWDRIVER | LEE, JAE B. |
| | | | | | |

| 07147245 | Not Issued | 161 | | LENS FOR CORRECTING THE LEE, JAE B. IMAGES COME FROM FOCUSING CONCAVE LENS | |
|----------|---------------|-----|------------|--|---------------|
| 07320573 | Not Issued | 169 | l I | SCREW-TYPE FASTENING DEVICE.A. | LEE, JAE B. |
| 07320618 | Not Issued | 169 | 03/08/1989 | SCREW TYPE FASTENING DEVICE WITH DOUBLE SPIRALS | LEE, JAE B. |
| 07005221 | Not Issued | 161 | 03/10/1987 | FOCUSING CONCAVE LENS, TELEPHOTO LENS, AND CAMERA LENS FOR PHOTOGRAPHING AND SEARCHING | LEE, JAE BEOM |
| 07186352 | 4810149 | 150 | 11 1 | SCREW-TYPE FASTENING DEVICE | LEE, JAE BEOM |
| 07186389 | 4850775 | 150 | | SCREW-TYPE FASTENING DEVICE | LEE, JAE BEOM |
| 10565255 | Not Issued | 30 | 01/19/2006 | Silicon film bulk acoustic wave device and process of the same | LEE, JAE BIN |
| 10182244 | Not Issued | 161 | 05/23/2003 | Method for manufacturing a shadow mask | LEE, JAE BOK |
| 11023620 | Not Issued | 93 | 12/29/2004 | THIN FILM TRANSISTOR SUBSTRATE OF A HORIZONTAL ELECTRIC FIELD TYPE AND METHOD OF DARKENING DEFECTIVE PIXEL IN THE SAME | LEE, JAE BONG |
| 11451121 | Not Issued | 19 | | Submerged-type electrosorption- based water purification apparatus and method thereof | LEE, JAE BONG |
| 10667662 | Not Issued | 30 | | Method and apparatus for displaying digital broadcasting | LEE, JAE BUNM |
| 06932876 | Not Issued | 161 | 11/20/1986 | RINGPOLE | LEE, JAE C. |
| 07453594 | Not Issued | 161 | 12/20/1989 | PIEZOELECTRIC CERAMIC COMPOSITION | LEE, JAE C. |
| 07736035 | 5099379 | 150 | 01/25/1991 | SURGE PREVENTING CIRCUIT FOR ELECTROMAGNETIC INDUCTION COOKING APPARATUS | LEE, JAE C. |
| 07745347 | 5218266 | 250 | | COLOR PICTURE TUBE HAVING RESIDUE ELECTRON REMOVING ASSEMBLY | LEE, JAE C. |
| 08166864 | 5405965 | 150 | 11 | PROCESSES FOR PREPARATION OF 5- | LEE, JÄE C. |

| | | | | PYRAZOLEMERCAPTAN DERIVATIVES AND INTERMEDIATES THEREOF | |
|----------|----------------|-----|-------------|---|---------------|
| 08233432 | Not Issued | 161 | | METHOD FOR THE PREPARATION OF THERMAL RESISTANT AND CHLORINE RESISTANT ELASTIC FIBER | LEE, JAE C. |
| 08341893 | 5416220 | 150 | 11/15/1994 | PROCESSES FOR PREPARATION OF 5- PYRAZOLEMERCAPTAN DERIVATIVES | LEE, JAE C. |
| 08350979 | 5534637 | 150 | 11/29/1994 | STARTING MATERIALS FOR PREPARATION OF SULFONYLUREA DERIVATIVES AND PROCESS FOR PREPARING THE SAME | LEE, JAE C. |
| 08355197 | 5509283 | 150 | 12/08/1994 | CLOTHES WASHER HAVING WATER RECIRCULATION SYSTEM | LEE, JAE C. |
| 08377642 | 5648129 | 150 | 01/25/1995 | MELODIC PARTY-FAVORS | LEE, JAE C. |
| 08386825 | 5468896 | 150 | B · · · · I | DITHIOCARBAZATE COMPOUNDS | LEE, JAE C. |
| 08386867 | 5466854 | 150 | 02/10/1995 | PROCESSES FOR PREPARATION OF DITHIOCARBAZATES | LEE, JAE C. |
| 08389042 | 5525730 | 150 | 02/15/1995 | PROCESSES FOR PREPARATION OF SULFONYLUREA DERIVATIVES | LEE, JAE C. |
| 08534981 | Not Issued | 161 | 09/27/1995 | PROCESS FOR THE PREPARATION OF VISCOSITY- STABLE POLYURETHANIC ELASTOMER | LEE, JAE C. |
| 10329508 | 6819289 | 150 | 12/27/2002 | CHIP ANTENNA WITH PARASITIC ELEMENTS | LEE, JAE CHAN |
| 10722543 | <u>6980155</u> | 150 | 11/28/2003 | INTERNAL DIVERSITY ANTENNA | LEE, JAE CHAN |
| 10803907 | Not Issued | 30 | 03/19/2004 | Method and apparatus for calculating an average picture level and plasma display using the same | LEE, JAE CHAN |
| 11007200 | 7042414 | 150 | 12/09/2004 | ULTRA WIDEBAND INTERNAL ANTENNA | LEE, JAE CHAN |
| 11092187 | Not Issued | 93 | 03/29/2005 | ULTRA WIDEBAND INTERNAL ANTENNA | LEE, JAE CHAN |

| 11320197 | Not Issued | 30 | 12/28/2005 | Chip antenna | LEE, JAE CHAN |
|----------|---------------|-----|------------|--|-------------------|
| 10873265 | Not Issued | 30 | | Automatic frequency controlling apparatus and method of mobile communication terminal | LEE, JAE CHEOL |
| 09853613 | 6760942 | 150 | | WASHING PATTERN DETERMINATION METHOD AND APPARATUS | LEE, JAE CHEON |
| 11139778 | Not Issued | 20 | 05/31/2005 | Apparatus and method for compensating for color of video signal in display device | LEE, JAE CHEON |
| 08856086 | 5946947 | 150 | | CLOTHES WASHING MACHINE HAVING VIBRATION AND NOISE DAMPER | LEE, JAE CHEON |
| 09213419 | Not Issued | 161 | 12/17/1998 | A SILICON WAFER HAVING A HEXAGONAL SEMICONDUCTOR CHIP REGION AND A METHOD FOR OBTAINING A HEXAGONAL SEMICONDUCTOR CHIP | LEE, JAE CHEON |
| 10099781 | 6998487 | 150 | 03/14/2002 | DOUBLE-SPIRO ORGANIC COMPOUNDS AND ORGANIC ELECTROLUMINESCENT DEVICES USING THE SAME | LEE, JAE CHOL |

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